



## Candidate Statement 2010 Election

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**Candidate Name:** John Kibarian  
**Title:** CEO & President  
**Company:** PDF Solutions, Inc.

### **Candidate Statement:**

During the past 4 years as an active Board Member of the EDA Consortium, I have contributed a balanced view of the issues that the EDA industry faces related manufacturing challenges. These issues impact how we perceive the problems that our customers will face and the opportunities that our industry can strive to capture for reigniting growth. I have also served on the board to promote and address the needs that each of our businesses faces in terms of standards, export controls, conferences and tradeshow, and market data.

PDF Solutions has built strong relationships with leading foundries, fables, fablite, and IDMs by meeting their needs for silicon characterization and the applications that depend upon rich characterization data. In serving these clients, we've gained an appreciation for the need to integrate this data into their design flows. This has led PDF Solutions to collaborate with several EDA companies to meet these integration needs.

As the semiconductor industry drives to advanced nodes, addressing the challenges that lie at the interface between design and manufacturing will become critically important for the EDA industry. In this environment, I want to continue to serve as a director to promote the EDA Consortium goals for fostering industry wide communication and innovation to address these challenges. I also want to ensure that our programs are effective in creating a deeper sense of appreciation among our mutual customer and investor bases for the value that we as an industry are delivering.

I would appreciate the opportunity to serve on the board, and ask for your vote.

Thank you.

**John Kibarian**

Biography:

Dr. Kibarian has served as CEO and president of PDF Solutions ([www.pdf.com](http://www.pdf.com)) since 2000 and as president since founding the company in 1991.

Before launching the company, Dr. Kibarian was a researcher at Carnegie Mellon University's SEMATECH Center for Rapid Yield Learning. He developed algorithms for diagnosing process variations based on electrical test data and for yield maximization based on circuit optimization and statistical simulation of processes and devices. He holds B.S. and M.S. degrees in Electrical Engineering and a Ph.D. in Computer Engineering, all from Carnegie Mellon University.

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