



CEO Forecast & Industry Vision

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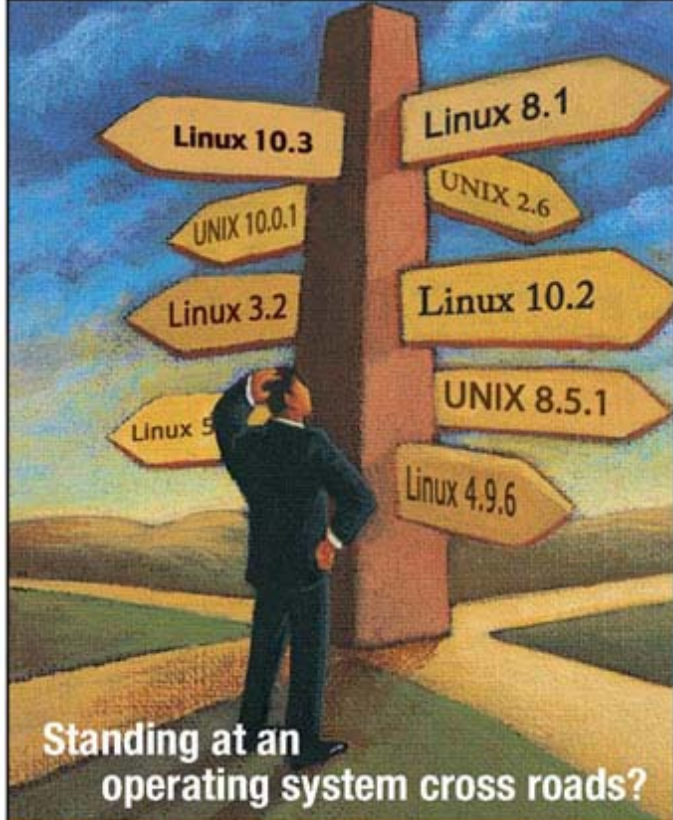
Walden C. Rhines
Chairman

123 EDA Consortium Member Companies

@HDL	BindKey Technologies	FTL Systems	Nassda Corporation	Silicon Dimensions
ACAD	Cadence Design Systems	GigaScale IC	Novas Software	Silvaco Data Systems
AccelChip	Calypto Design Systems	Golden Gate Technology	Novell	Spiratech Limited
ACE Associated Compiler Experts	Carbon Design Systems	Hewlett-Packard	Obsidian Software	Stelar Tools
Adventest America R&D Center	Celoxica	IBM	OEA International	Sun Microsystems
Agilent Technologies	Certess	IC Manage	Orora Design Technologies	Synfora
Aldec	ChipMD	Incentia Design Systems	Platform Computing	Synopsys
Alternative System Concepts	ChipVision Design Systems AG	Intel Corporation	Prolific	Synplicity
AMD	CiraNova	Interra Systems	Prosilog SA	SynTest Technologies
AmmoCore Technology	Clear Shape Technology	iRoC Technologies	Prudential Equity Group	Syntricity
Ansoft Corporation	Concept Engineering	Jasper Design Automation	Pulsic Limited	Takumi Technology Corporation
Apache Design Solutions	CoWare	Legend Design Technology	RBC Capital Markets	Tenison EDA
APLAC Solutions Inc.	CriticalBlue	Library Technologies	Real Intent	Tensilica
Applied Wave Research	DAFCA	LogicVision	Red Hat	Tera Systems
Aprio Technologies	Denali Software	Magma Design Automation	ReShape	Tharas Systems
Artisan Components	Deutsche Bank	Manhattan Routing	S2C – System2Chip	TransEDA Technology Ltd.
Athena Design Systems	Doulos Ltd.	MathWorks	S3 - Silicon & Software Systems	TriCN
Atrenta	Eagleware Corporation	MatrixOne	Sagantec	VaST Systems Technology
Avertec	EDA Centrum	Mentor Graphics Corporation	Sandwork Design	Verific Design Automation
Azuro	Engineering DataXpress	Merrill Lynch	Sequence Design	Verisity Design
Bank of America Securities	Esterel Technologies	Monterey Design Systems	Shearwater Group	Virage Logic Corporation
Barcelona Design	EVE-USA	MP Associates	SI2 - Silicon Integration Initiative	Virtual Silicon Technology
Beach Solutions	E-Z-CAD	Nascentric	Sierra Design Automation	Xpedion Design Systems
Berkeley Design	Fintronic USA		Sigma-C	Y Explorations
	Forte Design Systems		Sigrity	Zenasis Technologies
			Silicon Design Systems	

Industry OS Roadmap

- **Newly updated**
 - based on survey of member companies
- Available at www.edac.org
- Advertising at **DATE & DAC**




Standing at an operating system cross roads?

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Current OS Roadmap Participants

Aldec

Ammocore
Technology

Apache Design
Solutions

Aptix Corporation

Atrenta

Bindkey

Cadence

Carbon Design
Systems

ChipMD

Concept Engineering

CoWare

Fintronic

FTL Systems, Inc.

Interra Systems

LogicVision

Magma Design
Automation

Mentor Graphics

Nassda Corporation

ReShape

Sequence Design

Sagantec

Silicon Dimensions

Synapps Software

Synopsys

Synplicity

Summit Design

Tenison EDA

Verisity

- To participate, email your company link to karla@edac.org

CEO Forecast Panel 2005

- **Moderator:**
Jay Vleeschhouwer, Merrill Lynch

- **Panelists:**

Mike Fister, Cadence

Sanjay Srivastava, Denali Software

Jim Douglas, ReShape

Wally Rhines, Mentor Graphics

Aart de Geus, Synopsys

Chris Rowan, Tensilica

**(Jay's and the panelists' slides will
begin here)**

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WHERE
ELECTRONICS
BEGINS