



**Aart de Geus**  
Chairman

# 110 EDA Consortium Member Companies

ACE Associated Compiler Experts

Adventest America R&D Center

Agilent Technologies

Altium Limited

Apache Design Solutions

Applied Wave Research

Aprio Technologies

ArchPro Design Automation

Athena Design Systems

Atrenta

Axiom Design Automation

Azuro

Beach Solutions

Berkeley Design

Blaze DFM

Blue Pearl Software

Bluespec, Inc.

Brion Technologies

Cadence Design Systems

Calypto Design Systems

Carbon Design Systems

Catalytic

CebaTech

Celoxica

Certess

Chip Estimate (GigaScale IC)

ChipVision Design Systems

Clear Shape Technology

ClioSoft, Inc.

Concept Engineering

CoWare

CriticalBlue

DAFCA

Denali Software

Doulos Ltd.

EDA Centrum

edXact

Engineering DataXpress

EVE-USA

Flomerics

Forte Design Systems

FTL Systems

Golden Gate Technology

Gradient Design Automation

Hewlett-Packard

IC Manage

Imperas

Intel Corporation

Interra Systems

Jasper Design Automation

Javelin Design Automation

Jeda Technologies

Kimotion Technologies

Knowlent Corporation

Legend Design Technology

Library Technologies

LogicVision

Lorentz Solution

Lynguent

Magma Design Automation

Manhattan Routing

MatrixOne

Mentor Graphics Corporation

Merrill Lynch

MIPS

MP Associates

Nangate

Nannor Technologies

Nascentric

NEC Informatec, Ltd.

Novas Software

Novell

Obsidian Software

OCP International Partnership

OEA International

OneSpin Solutions

PDF Solutions

Ponté Solutions

Prolific

Pulsic Limited

Pyxis Technology

RBC Capital Markets

Real Intent

Retrinsic

Sagantec

Sandwork Design

Sequence Design

Sierra Design Automation

Silicon Integration Initiative

Sigrity

Silicon Canvas

Simucad Design Automation

SoftJin Technologies Pvt. Ltd.

Spiratech Limited

Sun Microsystems

Synopsys

Synplicity

SynTest Technologies

Takumi Technology Corporation

Target Compiler Technologies

Tenison Design Automation

Tensilica

The MathWorks

Tool Corporation

Tuscany Design Automation

VaST Systems Technology

Verific Design Automation

Virage Logic Corporation

Y Explorations

Zenasis Technologies

# EDA Consortium Staff



**Robert Gardner**  
Executive Director



**Karla Haley**  
Marketing Manager



**Jennifer Meola**  
Project Manager

# EDA Consortium Board for FY 2007-2008



**Aart de Geus, Ph.D.**  
EDA Consortium Chairman  
Chairman & CEO, Synopsys


**Executive Director**  
Robert Gardner

**CFO/Treasurer**  
Robert Gardner


**Secretary**  
Mark White  
White & Lee, LLP




**Kathryn Kranen**  
EDA Consortium Vice Chairman  
President & CEO, Jasper Design




**Walden C. Rhines, Ph.D.**  
EDA Consortium Vice Chairman  
Chairman & CEO, Mentor Graphics




**John Bourgojn**  
EDA Consortium Director  
President & CEO, MIPS Technologies



**Michael J. Fister**  
EDA Consortium Director  
President & CEO, Cadence Design



**John Kibarian**  
EDA Consortium Director  
President & CEO, PDF Solutions



**Alan P. Naumann**  
EDA Consortium Director  
President & CEO, CoWare



**Scott Sandler**  
EDA Consortium Director  
President & CEO, Novas Software Inc.



**Sanjay Srivastava**  
EDA Consortium Director  
President & CEO, Denali Software



# Committee Chairmen & Vice Chairs



Anti-Piracy Chairman  
**Scott Baeder**  
Senior Architect  
Cadence Design Systems Inc.



Communications Committee Chairman  
**Ryerson Schwark**  
Director of Public & Investor Relations  
Mentor Graphics Corporation



Emerging Companies Chairman  
**Stephen Pollock**  
VP of Marketing & Sales  
JEDA Technologies, Inc.



Export Chairman  
**Larry Disenhof**; Group Director  
Export Compliance & Gov. Relations  
Cadence Design Systems



Interoperability Chairman  
**Rich Goldman**  
VP Strategic Market Development  
Synopsys, Inc.



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Executive Director  
EDA Consortium

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**Michael McNamara**  
VP & General Manager CTS  
Cadence Design Systems Inc.

Vice Chairman:  
**Mark Noneman**  
VP Enterprise Quality  
Cadence Design Systems Inc



Trade Show Liaison Chairwoman  
**Anne Cirkel**  
Director Corporate Marketing  
Mentor Graphics Corporation

# In Memory of A. Richard Newton



# In Memory of A. Richard Newton



**“We really are a global community; not just businesses, not just technology; we are one family.**

**If you believe that people are a net positive, and I do, then technology can and must be used in the interest of society.”**

**A. Richard Newton**

# Dean A. Richard Newton Memorial Professorship



- Awarded to a UC Berkeley College of Engineering faculty member working in the field of synthetic biology.
- Synthetic biology seeks not only to understand biological systems, but also to go beyond knowledge in order to design biological components.
- These synthesized compounds or even organisms have the potential to address a host of problems that face our society.
- For contributions to the Dean A. Richard Newton Memorial Professorship, please visit [www.edac.org](http://www.edac.org).

# Phil Kaufman Award

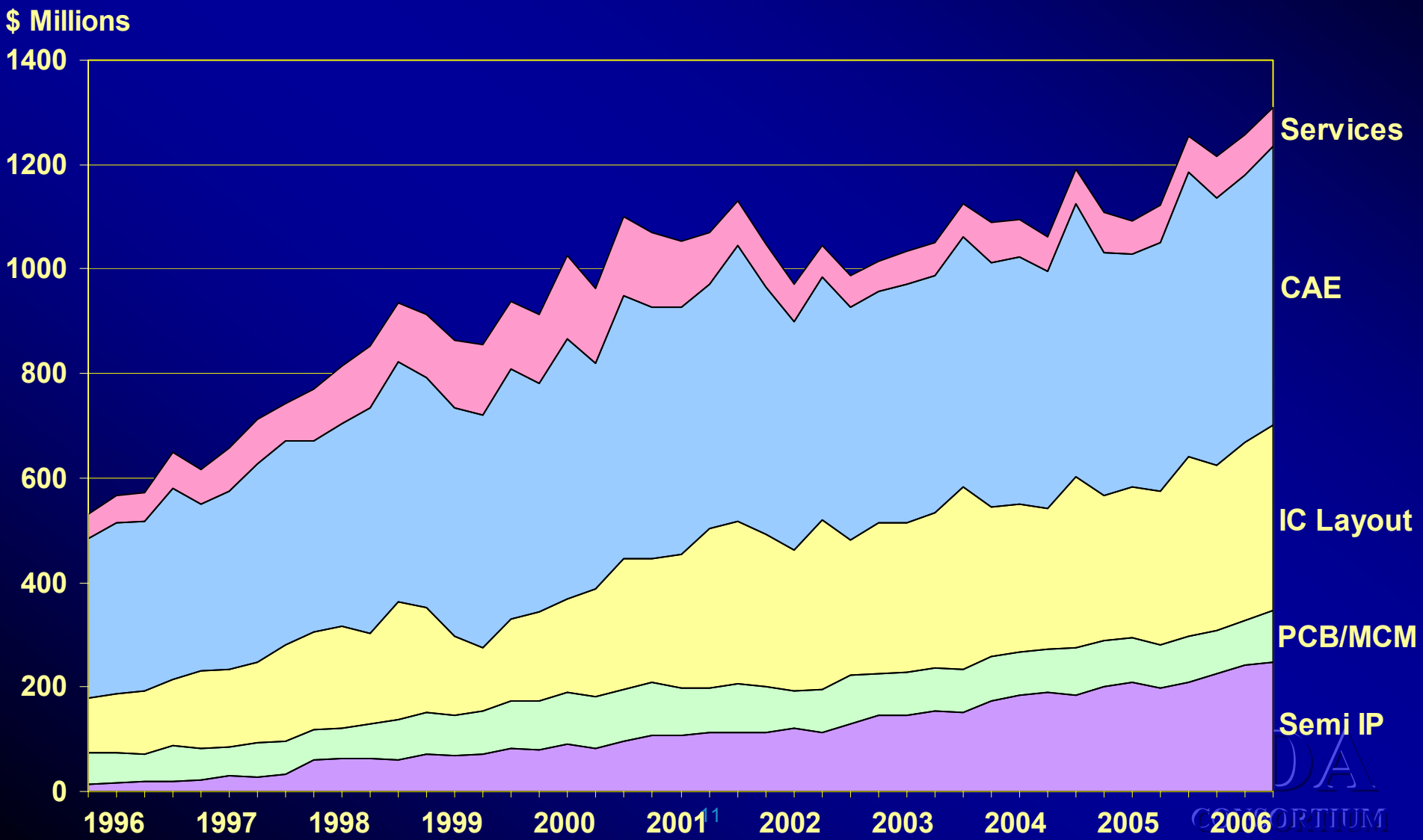


- **Awarded to individuals who have had demonstrable impact on the field of electronic design through contributions in EDA in the categories of:**
  - Business
  - Industry Direction & Promotion
  - Technology & Engineering
  - Educational & Mentoring
- **Sponsored by the EDA Consortium and the IEEE Council on EDA**
- **Robert Dutton is this year's award recipient for his contributions in the area of computer simulation of IC technology**
- **Deadline to nominate next year's recipient is June 29<sup>th</sup>**
- **Forms available at [www.edac.org](http://www.edac.org)**

# EDA Consortium Scheduled Events

<b>April 17, 2007</b>	<b>Executive Reception / DATE</b>	<b>Nice, France</b>
<b>May 1, 2007</b>	<b>Spring Member's Meeting</b>	<b>Silicon Valley, CA</b>
<b>May 10, 2007</b>	<b>East Cost Meeting</b>	<b>Boston, MA</b>
<b>June 3, 2007</b>	<b>Executive Briefing / DAC</b>	<b>San Diego, CA</b>
<b>June 6, 2007</b>	<b>Innovation Luncheon / DAC</b>	<b>San Diego, CA</b>
<b>June 6, 2007</b>	<b>Hacks &amp; Flacks Roundtable / DAC</b>	<b>San Diego, CA</b>
<b>Nov. 1, 2007</b>	<b>Executive Forum</b>	<b>Santa Clara, CA</b>
<b>Nov. 1, 2007</b>	<b>Kaufman Award Dinner</b>	<b>Santa Clara, CA</b>

# MSS Shows Continued Industry Growth



# Q3 2006 Leading Most Categories

## MSS Q3 2006 Growth Summary

	Average Annual Growth Rate	Q3 2006 / Q3 2005 Growth
EDA License Revenue	10.8%	16.1%
CAE	10.1%	12.5%
IC Physical Design & Verification	14.2%	19.4%
PCB & MCM	2.7%	24.4%
Semiconductor IP Revenue	16.5%	24.0%
Services Revenue	6.9%	3.5%
Total EDA, SIP & Services	11.5%	16.7%



# CEO Forecast & **Industry Vision**

February 22, 2006

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Sponsors



A/V by Munday & Collins



i n v e n t



# CEO Forecast & Industry Vision

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Moderator:

- Jay Vleeschhouwer, Merrill Lynch

Panelists:

- Aart de Geus, Synopsys, Inc.
- Mike Fister, Cadence Design Systems
- Alan Naumann, CoWare, Inc.
- Walden Rhines, Mentor Graphics Corp.
- Scott Sandler, Novas Software