



Synplicity[®]

Simply Better Results

EDA Consortium 2006 Forecast

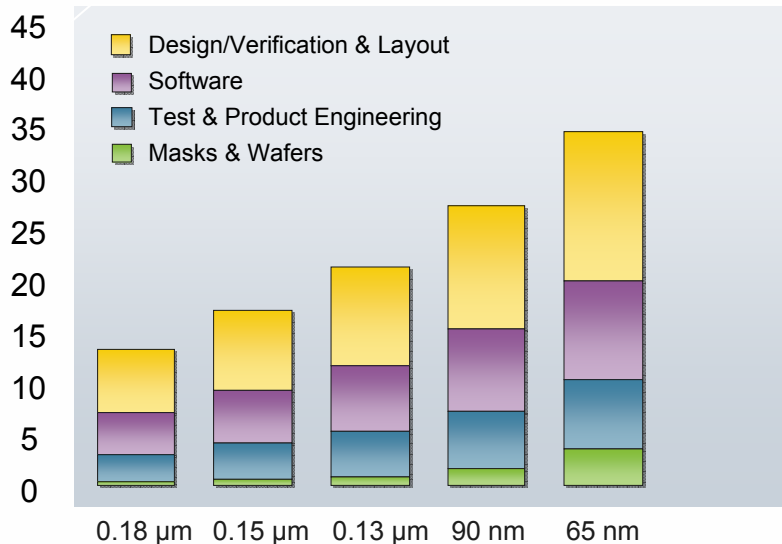
Gary Meyers
February 2, 2006

Rising Technology Costs

Cause:

Development Costs Rising

Total Development Costs (\$M)

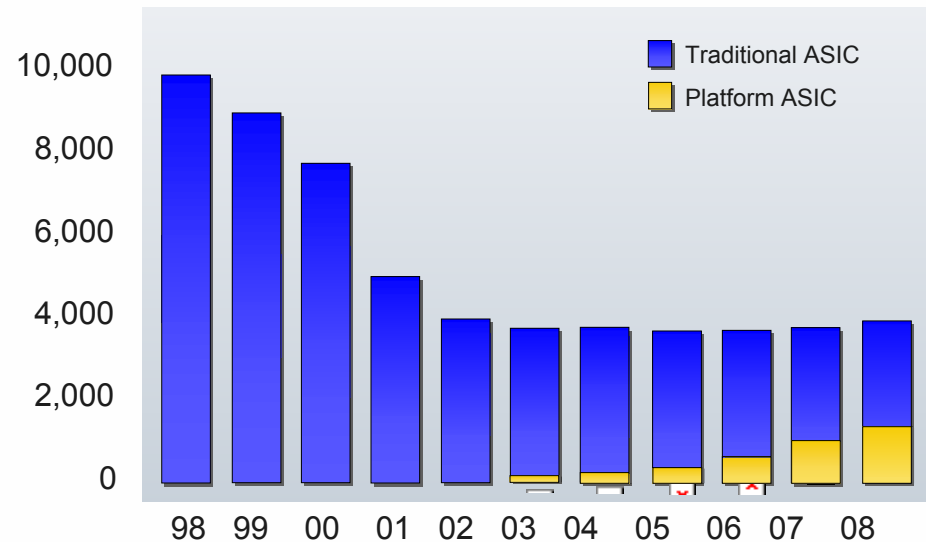


Source: IBS (March 2005)

Effect:

Fewer Custom Design Starts

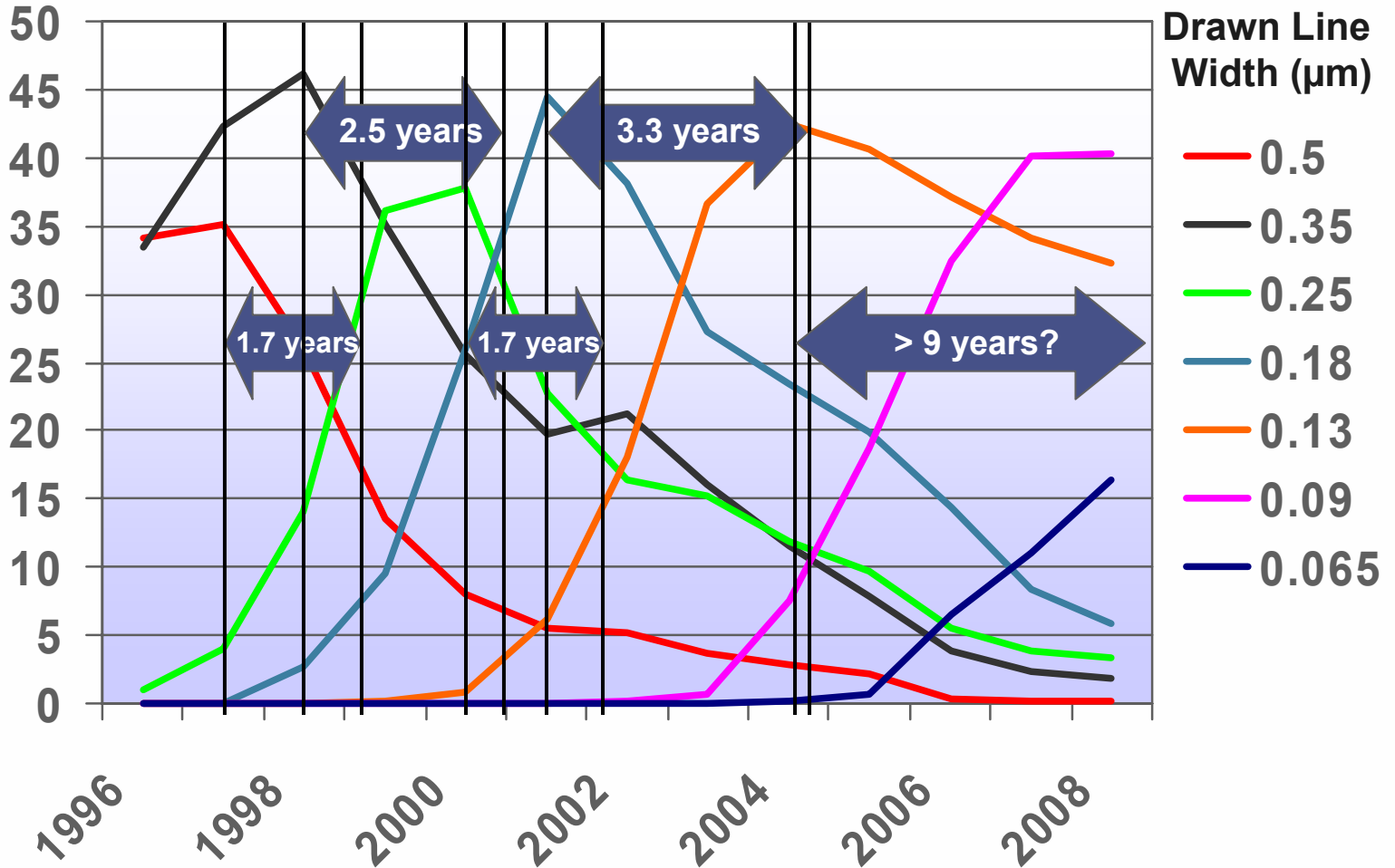
Number of ASIC Design Starts



Source: Gartner Dataquest (June 2005)

Slowing Lithography Transitions

% of New Design Starts

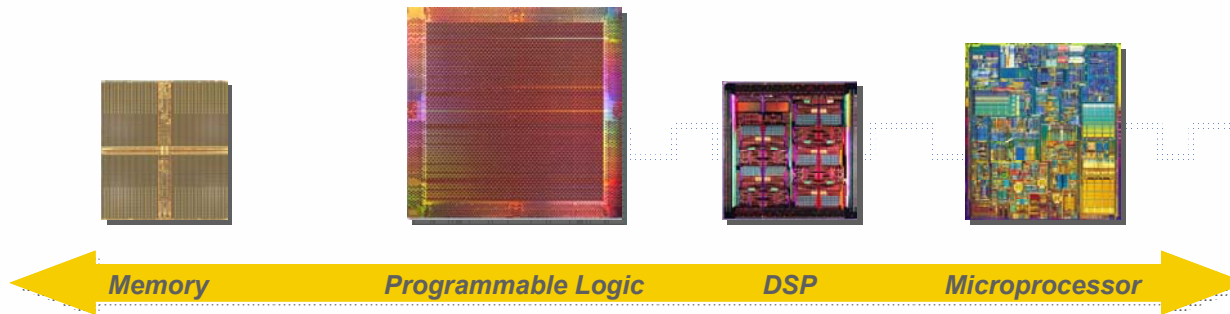


Source: Gartner Dataquest (1999, 2003, Aug 2005)

FPGAs Move Beyond ASIC Replacements

“FPGAs are programmable devices. If we can appeal to software engineers, we can increase our customer base by at least 10x. That's really where our future is.”

- Wim Roelandts, Xilinx CEO



“We see a very strong opportunity, moving forward, replacing ASSPs.”

- John Daane, Altera CEO

- **FPGA ESL Approaches Grow and Drive Market**
- **New FPGA Entrants Sample Silicon**
- **Continued Growth in Structured ASICs**
- **Stable Single Digit EDA Growth**



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End